

SP8690 200MHz ÷ 10/11

SP8691 200MHz ÷ 8/9

The SP8690 and SP8691 are low power ECL variable modulus dividers, with both ECL10K and TTL/CMOS compatible outputs. They divide by the lower division ratio when either of the ECL control inputs, PE1 or PE2, is in the high state and by the higher ratio when both are low (or open circuit).

FEATURES

- ECL and TTL/CMOS Compatible Outputs
- AC-Coupled Input
- Control Inputs ECL Compatible

QUICK REFERENCE DATA

- Supply Voltage: -5.2V ± 0.25V (ECL), 5V ± 0.25V (TTL)
- Power Consumption: 70mW (Typ.)
- Temperature Range:
 - 55°C to +125°C (A Grade)
 - 30°C to +70°C (B Grade)

ABSOLUTE MAXIMUM RATINGS

| | |
|--|-----------------|
| Supply voltage, V _{CC} - V _{EE} | 8V |
| ECL output current | 10mA |
| Storage temperature range | -65°C to +150°C |
| Max. junction temperature | +175°C |
| TTL output voltage | +12V |
| Input voltage | 2.5V p-p |
| Max. open collector current | 15mA |

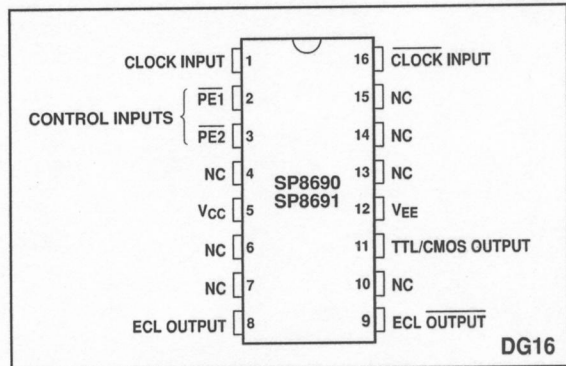


Fig. 1 Pin connections - top view

ORDERING INFORMATION

- SP8690 A DG
- SP8690 B DG
- SP8691 A DG
- 5962-87678 (SMD) (SP8690)

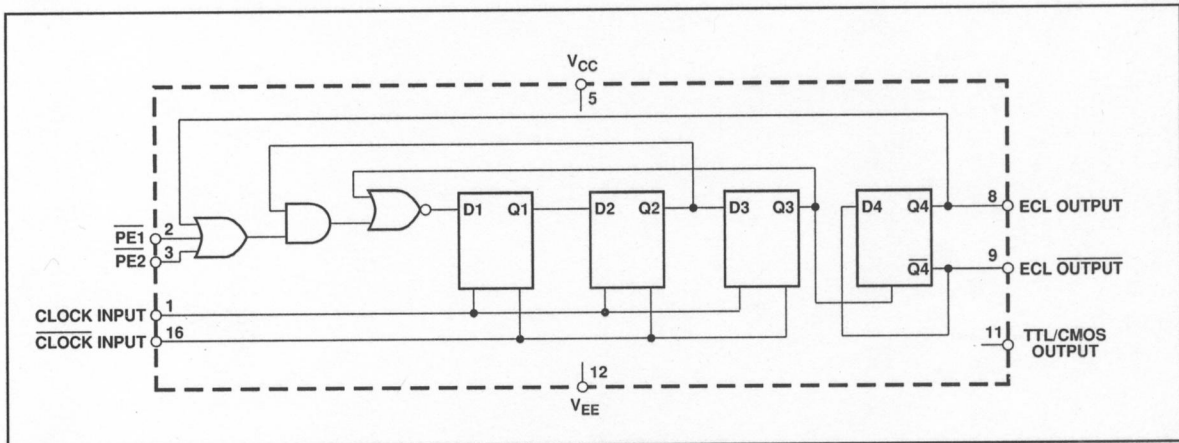


Fig. 2 Functional diagram (SP8690)